



DOEACC CENTRE (formerly CEDTI), AURANGABAD

A unit of DOEACC Society, New Delhi, An autonomous Scientific Society of Department of Information Technology, Ministry of Communications & Information Technology, Govt. of India)

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Recruitment

Advt.No.REC/01/10

DOEACC Centre (formerly CEDTI), Aurangabad, A Unit of DOEACC, An Autonomous Scientific Society of Department of Information Technology, Ministry of Communications and Information Technology, Govt. of India, New Delhi, is a premier HRD organization. It has been established primarily to develop quality manpower and skilled professional in the field of Information, Electronics, Communications Technology (IECT). The Centre offers industry specific quality training at affordable cost (AICTE approved two years M.Tech Electronics Design & Technology (EDT) and three years "Diploma in Electronics Production and Maintenance" (DEPM) and Short Term Courses (STCs) in the state-of-the-art areas i.e. Embedded System Design, VLSI Design, Information Security, e-Learning, Agri Electronics, Opto Electronics, Power Electronics, IT, Industrial Design, CAD/CAM, etc.). The Centre also undertakes innovative electronics product design and development & projects, offers consultancy and services to industries and implements DOEACC Software, Hardware and ITES-BPO Schemes. The Centre is looking for dynamic, devoted and result oriented candidates for the following posts on direct recruitment on regular basis except for the post mentioned at Sr.No.08, to play a key role in Academic, R&D, Schemes and Projects in the advanced fields of IECT other than the assigned duties.

01.Scientist - D : Two (2) posts (UR) (one post each for Comp. Engg./IT and Agri-Electronics group)

(i) Scale of Pay : PB3 Rs.15600-39100 + GP Rs.7600 (total emoluments at min. is Rs. 50,995/- approx.)

(ii) Upper Age Limit : 40 years as on 30.11.2010.

(iii) **Qualification & Experience** : BE/B.Tech OR ME/M.Tech or recognised equivalent degree in the field of Electrical/ Electronics/ECE/Instrumentation/Comp. Engg. or Science with minimum 60% marks. OR Ph.D. (Engg./Comp. Sc.) with 9/8/5 years relevant experience respectively should be in the just next lower or equivalent grade for those working in Govt./Autonomous Bodies/ PSUs in Public/ Private Sector respectively. Out of the above mentioned experience, minimum 3 years should be in the scale, having GP Rs.6600 (Pre-revised Rs.10,000-15,200 or equivalent). The degree should be from a recognised Univ./Institution.

(iv) **Fields of Specialisation** : The relevant experience in a responsible position should be in teaching and E & T management or R&D or relevant product design &/or dev. in industry. Ability to lead/guide a team in their respective areas of specialisation with very good communication skill shall be desirable. (i) For the post in Agri-electronics, specialised areas are Process Control Instrumentation & Automation, knowledge of Virtual Instrumentation/Analysis & its Tools, Application of Electronics &/or IT to Agri-sector & Processing Industry. The desirable specialization are knowledge of modern electronics design and RFI, EMC & Reliability. (ii) For the post in IT, specialised areas are H/W, Networking, Open Source Technology, Proj. Mgmt., S/W Architecture & Programming Languages, S/W Tools & Various Interfaces, S/W Quality, Standards & Testing, Web/Internet Tech., Security and Portability. The desirable specialisations are knowledge of Information Security, E-learning Technologies, Advance OS, (Intelligent) Networking, Programming & S/W Dev., Business Process Automation S/W, UML and Rational Unified Process, e-governance and such emerging areas related to IT.

02. Scientist - C : Three (3) posts (2 UR, 1 OBC)

(One post each for VLSI & Embedded System (ES) Design, e-learning and Industrial Design (ID))

(i) Scale of Pay : PB3 Rs.15600-39100+GP Rs. 6600 (total emoluments at min. is Rs. 44,147/- approx.)

(ii) Upper Age Limit : 35 years as on 30.11.2010.

(iii) **Qualification & Experience** : BE/B.Tech with minimum 65% marks OR ME/ M.Tech or recognised equivalent degree in the field of Electrical/ Electronics/ ECE/ Instrumentation/ Comp.Engg. or Sc./ Mech./ Production OR Ph.D. (Engg./ Comp.Sc.) with 5/3/1 years relevant experience respectively should be in the just next lower or equivalent grade for those working in Govt./Autonomous Bodies/ PSUs in Public/Private Sector respectively. The degree should be from a recognised University/ Institution.

(iv) **Fields of Specialisation** : The relevant experience should be in teaching & acad. management or R&D or relevant product design &/or dev. in industry. Ability to lead/guide a team in their respective areas of specialisation with very good communication skill shall be desirable. (i) For the post in VLSI & ES Dgn., the specialised areas are Micro-electronics Devices Technology, VLSI & ES Dgn. & Tech., EDA Tools, Virtual Instrumentation/Analysis Tools, EDFM Dgn. Tools, System Dgn. & Optimisation, DSP, VHDL/VERILOG, H/W Testing & Verification Techniques OR H/W Dgn. and S/W Dev. Tools, Embedded Devices, RTOS, Programming & Wireless Embedded. The desirable specialisations are RFI, EMC & Reliability, Parallel Processing, MEMS based VLSI Design OR Advance RTOS, ES Programming, SoC. (ii) For the post in e-learning, specialised areas are e-Learning Technology & Tools, LMS & LCMS, Multimedia Systems & Technology Virtual Class Room/ Lab apart from the knowledge of IT, required for the field. The desirable specialisation are Visual Information Technology & recent trends in e-learning including standards & guidelines. (iii) For the post in ID, specialised areas are ID using automation tools for both 2 D & 3 D with aptitude towards electronics, knowledge & use of CAD/CAM/CAE Packages, Value Engineering. The desirable specialisations are Rapid Prototyping & its S/W, Mechatronics, Robotics, MEMS/ NEMS, Reliability, Eco-friendly Electronics Product Design and such emerging areas of ID.

03. Scientist - B : Six (06) posts (04-UR, out of these, one post for PH (OH), 02-OBC) One post each for IT (H/W), IT (S/W), Info. Security, VLSI Design, ES Design & Power Electronics, (PE).

(i) Scale of Pay : PB3 Rs.15600-39100+GP Rs. 5400 (total emoluments at min. is Rs. 36,970/- approx.)

(ii) Upper Age Limit : 30 years as on 30.11.2010.

(iii) **Qualification & Experience** : BE/B.Tech with minimum 65% marks OR ME/M.Tech or recognised equivalent degree in the field of Electrical/ Electronics/ ECE/ Instrumentation/ Comp. Engg. or Science. The degree should be from a recognised University/Institution.

(iv) **Fields of Specialisation** : (i) For the post in IT (H/W), knowledge of Computer Hardware, Networking, H/W Architecture & Interfaces, System & Network Management & Maintenance and related software are required. Knowledge of Open Source Technology, Advances in Computer System & Networking, Network & Information Security are desirable. (ii) For the post in IT (S/W), knowledge of OS, Data Comm., Database Structure, Storage & Management, S/W Architecture, Programming Languages & Tools, S/W User, Communication & Utility Interfaces, and their configuration & maintenance, Web/Internet Technologies, Office Automation S/W and related H/W are required. Knowledge of Proj. Mgmt., recent trends in S/W & its Quality/Standards, Testing & Maintenance, Dataware Housing & Mining, Security & Portability and Open Source Technology are desirable. (iii) For the post in the Information Security, knowledge of Network System, Data/ Information Security & Cyber Laws and their application, Security Audit & Compliance, Firewall & IDS Configurations are required. Knowledge of Cryptography, Enterprise Data Protection and recent trends in Information Security are desirable. (iv) For the post in VLSI Dgn., knowledge of Semiconductor Device Tech., VLSI Dgn. & Tech., EDA Tools, S/W Tools for Modelling/Simulation, Analysis & Synthesis, Modern Electronics Design Laboratory, VHDL/VERILOG, H/W Testing & Verification Techniques are required. Knowledge of RFI, EMC & Reliability, Virtual Instrumentation and recent trends in VLSI & ES Design are desirable.

The allowances are admissible as per the rules of DOEACC Society. Other benefits like EPF, Medical allowance, LTC shall be as applicable to the employees of DOEACC Society except for the post at Sr. No. 08. Age relaxation for SC/ST/OBC/PH/Ex-servicemen, Govt. employees etc. shall be as per Govt. of India rules. The interested, eligible candidates may send their applications giving details in a format mentioned below, neatly typed on A-4 size paper with a recent passport size photograph and a crossed demand draft of Rs.100/- for SC/ ST Rs. 50/- only and no fee for PH candidates) of a nationalized bank in favour of "Director, DOEACC Centre, Aurangabad", payable at Aurangabad (Maharashtra). Post applied for, Name, Father's Name, Date of Birth, Sex, Nationality, Category (with proof of certificates), Address & contact details i.e. Tel., Mobile, Fax, e-mail for Correspondence, Permanent Address, Educational/Professional Qualification (starting from 10th Std clearly indicating the examinations passed, year of passing, class & percentage of marks, subjects etc), Details of previous employments held with experience in chronological order indicating name of employer, period, nature of job performed, etc, Name & address of three referees and Signature of Applicant with Date. The application, complete in all respect covering above information superscribing the post applied for, should reach to the **Jt. Director (Admn & Finance), DOEACC Centre, University Campus, Aurangabad(MS) - 431 004 by not later than 30.11.2010.** No TA/DA shall be reimbursed to candidates appearing for interview. If the candidate claims his qualification as equivalent to the prescribed one, he is required to produce the documentary evidence to substantiate his claim.

For details, refer our website: www.doeaccaurangabad.org.in, www.doeacc.edu.in or www.employmentnews.gov.in

11. DETAILS OF EDUCATIONAL /PROFESSIONAL QUALIFICATIONS : (Starting from 10th Std)

Sr. No.	Examination/Degree	Year of Passing	Name of Board/University	Main Subject	% of Marks	Class/ Division

12. Details of present employment :

i.	Organisation	
ii.	Post	
iii.	Nature of Duties	
iv.	Pay Band & Gr. Pay / Scale of Pay/Total Emoluments	
v.	Period (From)	

13. Details of past experience (in chronological order) :

Sr. No.	Name & Address of The Employer	Post Held	Period		Pay Scale & Total Emoluments	Nature of Duties	Reason For Leaving
			From	To			

(Certificate may be attached separately)

14.	Whether Willing to be considered for Lower Grade/Post	
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15.	Minimum Initial Pay Acceptable	
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16.	Name & Address of Referees (Three Referees)		

16.	Any Other Information the Candidates would like to add		

DECLARATION

I hereby declare that I have carefully read the conditions of eligibility advertised and that I satisfy them. All the statements made in this application are true, complete and correct to the best of my knowledge and belief. I understand that in the event of any particular information given herein being found false or incorrect my candidature is liable to be rejected or cancelled and in the event of any misstatement/discrepancy in the particulars furnished and detected after my appointment, my services are liable to be terminated without notice. I undertake to abide by the decision of the selection authority.

Place:

Date:

(SIGNATURE OF THE CANDIDATE)

List of Enclosures :			
01		09	
02		10	
03		11	
04		12	
05		13	
06		14	
07		15	
08		16	

Important Note for Applicants :

- Those who are presently employed in Govt./Semi Govt./Autonomous/Public Sector organizations should either forward their application through proper channel or produce a 'NO OBJECTION CERTIFICATE' from the present employer at the time written test/Interview.
- The candidates applying for two or more posts should submit separate application for each post.
- Incomplete applications and applications not supported by copies of Certificates etc. wherever applicable shall be summarily rejected.
- No correspondence/inquiries in regard to the application submitted by the candidate shall be entertained.
- Canvassing in any type shall be a disqualification.
- The completed application form along with Fee, photograph (to be affixed), copies of certificates etc. is to be sent to the **“Joint Director, DOEACC CENTRE AURANGABAD, Dr. B. A. M. University Campus, Aurangabad – 431 004** in an envelope superscribed **“Application for the post of _____”** so as to reach him on or before the last date mentioned below.
- Last date of receipt of duly filled in application shall be **30th November, 2010.**